

Electronic Acknowledgement Receipt

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Application Number:	10643193
International Application Number:	
Confirmation Number:	3651
Title of Invention:	Circuits and methods for characterizing random variations in device characteristics in semiconductor integrated circuits
First Named Inventor/Applicant Name:	Azeez Bhavnagarwala
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Appeal Brief Filed	8728-635-Amd-Appeal-Brief.pdf	226682 1ec88d00d9f5fb4f46293d0f03a5a418f7cd4ecd	no	27

Warnings:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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